



On page 8, replace the paragraph beginning at line 9 with the following:

In an exemplary embodiment, fundamental processing units are defined by applying a profiling process. The fundamental processing units are parameterizable processing blocks that may be application specific but can be enabled for a variety of protocols. The profiling process is performed from a system and hardware perspective to optimize time sliced and multi-threaded architecture. Illustrative examples of fundamental processing units are the hardware kernels described in Figure 2 of co-pending U.S. Application Serial No. 09/772,584 entitled "A Wireless Spread Spectrum Communication Platform Using Dynamically Reconfigurable Logic." Additional information on the profiling process is provided in co-pending U.S. application serial no. 09/565,654, now U.S. patent no. 6,807,155, and entitled "Method of Profiling Disparate Communications and Signal Processing Standards and Services." These applications are commonly assigned and are hereby incorporated by reference for all purposes.